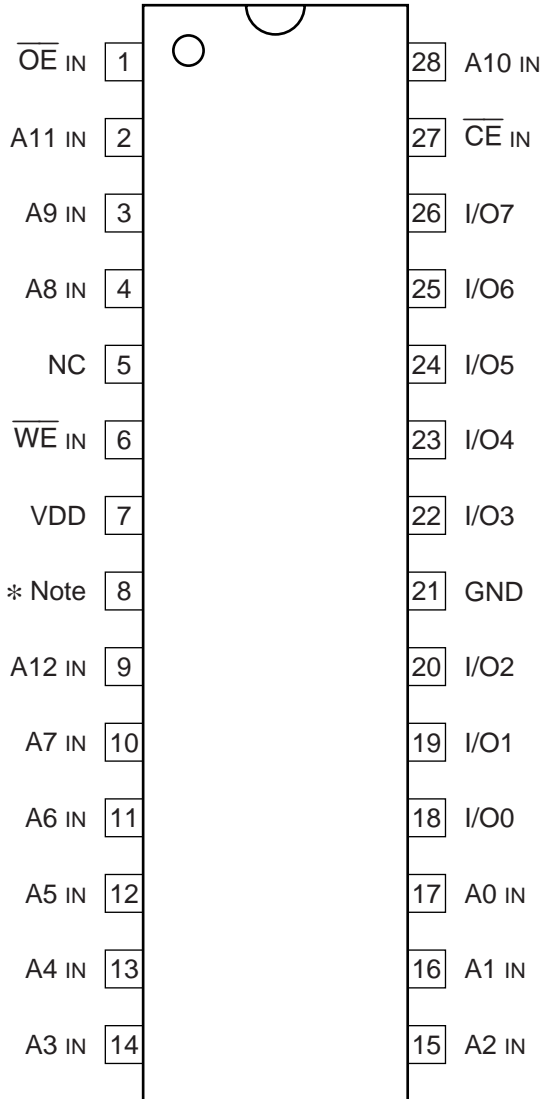


64 K (8 K × 8)-BIT EEPROM
 —TOP VIEW—



INPUTS

A0 - A12 : ADDRESS
 $\overline{\text{CE}}$: CHIP ENABLE
 $\overline{\text{OE}}$: OUTPUT ENABLE
 $\overline{\text{WE}}$: WRITE ENABLE

INPUTS/OUTPUTS

I/O0 - I/O7 : DATA

OUTPUT

$\overline{\text{RDY/BUSY}}$: READY/BUSY

OTHER

NC : NO CONNECTION

* Note

TYPE	PIN 8
AT28BV64	$\overline{\text{RDY/BUSY}}$
AT28C64	NC